TSDL8-F22MH8C Optical Transceiver

Single-mode 400G QSFP-DD ER8 40km Transceiver, With Diagnostic Monitoring

Features

- Hot-pluggable QSFP-DD form factor
- Compliant with IEEE std 802.3cnTM-2019:
- 400GBASE-ER8 optical interface
- 400GAUI-8 electrical interface
- Compliant with QSFP-DD MSA HW Rev 5.0
- Compliant with QSFP-DD CMIS Rev 4.0
- Class 1/ 1M Laser
- Complies with EU Directive 2011/65/EU (RoHS compliant)
- Duplex LC connector
- Digital diagnostics functions are available via the I2C interface
- Single 3.3V Power Supply and Power Dissipation < 14W
- Operating Case Temperature: 0°C~+70°C
- RoHS compliant

Applications

- 400GBASE-ER-8 400G Ethernet
- Telecom
- Data center

Production Description

This product is a 400Gb/s Quad Small Form Factor Pluggable-double density (QSFP-DD) optical module designed for 40km optical communication applications. The module converts 8 channels of 50Gb/s (PAM4) electrical input data to 8 channels of LWDM optical signals, and multiplexes them into a single channel for 400Gb/s optical transmission. Reversely, on the receiver side, the module optically de-multiplexes a 400Gb/s optical input into 8 channels of LWDM optical signals, and converts them to 8 channels of 50Gb/s (PAM4) electrical output data.

It contains a duplex LC connector for the optical interface and a 76-pin connector for the electrical interface. To minimize the optical dispersion in the long-haul system, single-mode fiber (SMF) has to be applied in this module.

The product is designed with form factor, optical/electrical connection and digital diagnostic interface according to the QSFP-DD Multi-Source Agreement (MSA) Type 2. It has been designed to meet the harshest

external operating conditions including temperature, humidity and EMI interference.

Absolute Maximum Rating

These values represent the damage threshold of the module. Stress in excess of any of the individual Absolute Maximum Ratings can cause immediate catastrophic damage to the module even if all other parameters are within Recommended Operating Conditions.

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCC	-0.5	+3.6	V
Storage Temperature	Tc	-40	+85	℃
Relative Humidity	RH	0	85	%

Recommended Operating Environment

Recommended Operating Environment specifies parameters for which the electrical and optical characteristics hold unless otherwise noted.

Parameter	Symbol	Min.	Typical	Max	Unit
Power Supply Voltage	VCC	3.135	3.30	3.465	V
Operating Case Temperature	T_{Ca}	0		70	℃

Electrical Characteristics

The following electrical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter	Symbol	Min.	Typical	Max	Unit	Notes
Data Rate per lane	DR	53.125±100ppm			Gbd	
Transmitter						
Input differential impedance	Rin	90	100	110	Ω	
Differential Input Voltage swing	Vin	900	-	-	mVp-p	
Receiver						
Differential Output Swing	Vout	-	-	900	mVp-p	
Output differential impedance	Rout	90	100	110	Ω	

Optical Characteristics

The following optical characteristics are defined over the Recommended Operating Environment unless otherwise specified.

Parameter		Symbol	Min.	Typical	Max	Unit	Notes
Center Wavelength	Ch0	λ0	1272.55	1273.55	1274.54	nm	
	Ch1	λ1	1276.89	1277.89	1278.89		
	Ch2	λ2	1281.25	1282.26	1283.27		
	Ch3	λ3	1285.65	1286.67	1287.68		
	Ch4	λ4	1294.53	1295.56	1296.59		
	Ch5	λ5	1299.02	1300.06	1301.09		
	Ch6	λ6	1303.54	1304.59	1305.63		
	Ch7	λ7	1308.09	1309.14	1310.19		
Transmitter							
Data rate, each lane			53.125±100ppm			GBd	
Side-mode Suppression Ratio		SMSR	30			dB	
Average Optical Power, per lane		Po	-0.6	-	5.6	dBm	1
Extinction Ratio		ER	6	-	-	dBm	
Transmitter and Dispersion Eye Closure		TDECQ			3.4	dB	
Optical Return Loss Tolerance		ORL	-	-	15	dB	
Receiver							
Data rate, each lane			53.125±100ppm			GBd	
Average Receive power ,each lane			-18.6		-4.4	dBm	
Receive power (OMAouter), each lane					-3.6	dBm	
Receiver sensitivity (OMAouter), each lane					Max(–16.1, SECQ – 17.5)	dBm	2

Note:

[1] Average launch power, each lane (min) is informative and not the principal indicator of signal strength. A transmitter with launch power below this value cannot be compliant; however, a value above this does not ensure compliance.

[2] Receiver sensitivity (OMAouter), each lane (max) is informative and is defined for a transmitter with SECQ of 1.4 dB.

Qsfp-Dd Transceiver Electrical Pad Layout

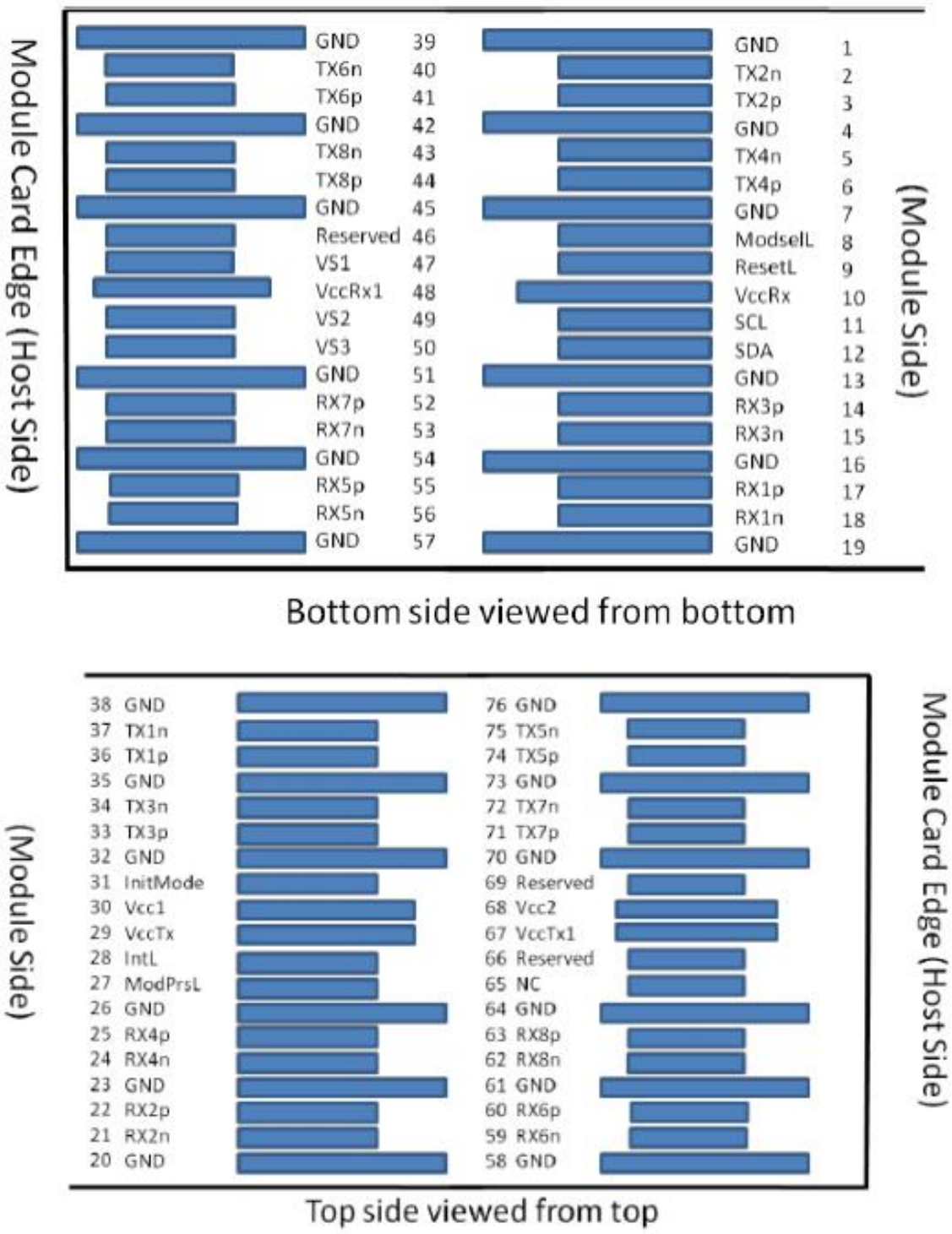

Figure 1 – QSFP-DD -compliant 76-pin connector (per QSFP-DD MSA)

Pin Definition

Pad	Logic	Symbol	Description
1		GND	Ground
2	CML-I	Tx2n	Transmitter Inverted Data Input
3	CML-I	Tx2p	Transmitter Non-Inverted Data Input
4		GND	Ground
5	CML-I	Tx4n	Transmitter Inverted Data Input
6	CML-I	Tx4p	Transmitter Non-Inverted Data Input
7		GND	Ground
8	LVTTL-I	ModSelL	Module Select
9	LVTTL-I	ResetL	Module Reset
10		VccRx	+3.3V Power Supply Receiver
11	LVCMOS-I/O	SCL	2-wire serial interface clock
12	LVCMOS-I/O	SDA	2-wire serial interface data
13		GND	Ground
14	CML-O	Rx3p	Receiver Non-Inverted Data Output
15	CML-O	Rx3n	Receiver Inverted Data Output
16		GND	Ground
17	CML-O	Rx1p	Receiver Non-Inverted Data Output
18	CML-O	Rx1n	Receiver Inverted Data Output
19		GND	Ground
20		GND	Ground
21	CML-O	Rx2n	Receiver Inverted Data Output
22	CML-O	Rx2p	Receiver Non-Inverted Data Output
23		GND	Ground
24	CML-O	Rx4n	Receiver Inverted Data Output
25	CML-O	Rx4p	Receiver Non-Inverted Data Output
26		GND	Ground
27	LVTTL-O	ModPrsL	Module Present
28	LVTTL-O	IntL	Interrupt
29		VccTx	+3.3V Power supply transmitter
30		Vcc1	+3.3V Power supply
31	LVTTL-I	InitMode	Initialization mode; In legacy QSFP applications, the InitMode pad is called LPMODE
32		GND	Ground
33	CML-I	Tx3p	Transmitter Non-Inverted Data Input
34	CML-I	Tx3n	Transmitter Inverted Data Input
35		GND	Ground
36	CML-I	Tx1p	Transmitter Non-Inverted Data Input
37	CML-I	Tx1n	Transmitter Inverted Data Input
38		GND	Ground

Pad	Logic	Symbol	Description
39		GND	Ground
40	CML-I	Tx6n	Transmitter Inverted Data Input
41	CML-I	Tx6p	Transmitter Non-Inverted Data Input
42		GND	Ground
43	CML-I	Tx8n	Transmitter Inverted Data Input
44	CML-I	Tx8p	Transmitter Non-Inverted Data Input
45		GND	Ground
46		Reserved	For future use
47		VS1	Module Vendor Specific 1
48		VccRx1	3.3V Power Supply
49		VS2	Module Vendor Specific 2
50		VS3	Module Vendor Specific 3
51		GND	Ground
52	CML-O	Rx7p	Receiver Non-Inverted Data Output
53	CML-O	Rx7n	Receiver Inverted Data Output
54		GND	Ground
55	CML-O	Rx5p	Receiver Non-Inverted Data Output
56	CML-O	Rx5n	Receiver Inverted Data Output
57		GND	Ground
58		GND	Ground
59	CML-O	Rx6n	Receiver Inverted Data Output
60	CML-O	Rx6p	Receiver Non-Inverted Data Output
61		GND	Ground
62	CML-O	Rx8n	Receiver Inverted Data Output
63	CML-O	Rx8p	Receiver Non-Inverted Data Output
64		GND	Ground
65		NC	No Connect
66		Reserved	For future use
67		VccTx1	3.3V Power Supply
68		Vcc2	3.3V Power Supply
69		Reserved	For Future Use
70		GND	Ground
71	CML-I	Tx7p	Transmitter Non-Inverted Data Input
72	CML-I	Tx7n	Transmitter Inverted Data Input
73		GND	Ground
74	CML-I	Tx5p	Transmitter Non-Inverted Data Input
75	CML-I	Tx5n	Transmitter Inverted Data Input
76		GND	Ground

Ordering Information

Part Number	Product Description
TSDL8-F22MH8C	400Gbps QSFP-DD ER8 Transceiver, up to 40Km transmission on single mode fiber (SMF), 0°C ~ +70°C

Important Notice

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