

TSBSD-85N-XXXX Active Optical Cable

Multi-mode 850nm 800G OSFP to QSFP-DD Active Optical Cable, With Diagnostic Monitoring

Features

- Hot-pluggable OSFP/QSFP-DD form factor
- Data Rate 106.25 Gb/s PAM4 per lane
- 8x106Gbps PAM4 transmitter and PAM4 receiver
- 8 channels 850nm VCSEL array
- 8 channels PIN photo detector array
- Power consumption < 14W per end
- CMIS V5.1 compliant
- Operating case temperature: 0°C~+70°C
- RoHS6 compliant (lead free)

Applications

- 800GBASE SR8 Ethernet

Description

The TSBSD-85N-XXXX is an 8-Channel, Pluggable, Parallel, Fiber-Optic OSFP to QSFP-DD AOC for Ethernet Applications. This AOC is a high performances module for short-range multi-lane data communication and interconnect applications. It integrates four data lanes in each direction with 8x53.125GBd. Each lane can operate at 106.25Gbps up to 30m using OM3 fiber. These modules are designed to operate over multimode fiber systems using a nominal wavelength of 850nm. The electrical interface uses a 60@OSFP and 76@QSFP-DD contacts edge type connector.

Absolute Maximum Ratings

These values represent the damage threshold of the module. Stress in excess of any of the individual Absolute Maximum Ratings can cause immediate catastrophic damage to the module even if all other parameters are within Recommended Operating Conditions.

Parameters	Symbol	Min.	Max.	Unit
Power Supply Voltage	VCC	-0.5	+3.6	V
Storage Temperature	Tc	-40	+85	°C
Relative Humidity ¹	RH	15	85	%

Notes:

1. Non-condensing.

Recommended Operating Environment

Recommended Operating Environment specifies parameters for which the electrical and optical characteristics hold unless otherwise noted.

Parameter	Symbol	Min	Typical	Max	Unit
Power Supply Voltage	VCC	3.15	3.30	3.45	V
Operating Case Temperature	Tca	0	-	70	°C

Electrical Characteristics

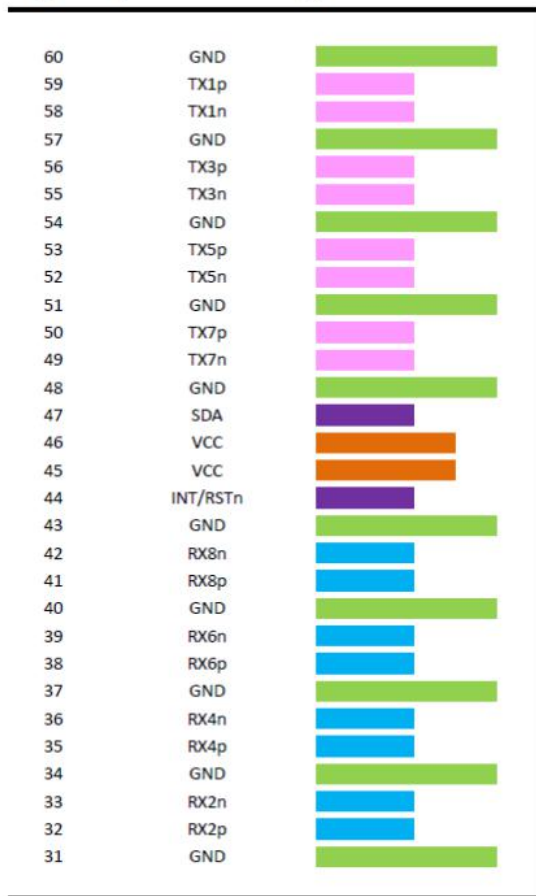
Parameter	Symbol	Min.	Typical	Max	Unit
Differential Input Voltage Amplitude ¹	Vin	300	-	1100	mV
Differential Output Voltage Amplitude ²	Vout	300	-	900	mV
Signaling rate per lane	DR	53.1255± 100 ppm			GBps
Differential termination mismatch	-	-	-	10	%
Skew	-	-	-	300	ps
Bit Error Rate ³	BER	-	-	2.4E-4	-
Input Logic Level High	VIH	2.0	-	VCC	V
Input Logic Level Low	VIL	0	-	0.8	V
Output Logic Level High	VOH	VCC-0.5	-	VCC	V
Output Logic Level Low	VOL	0	-	0.4	V

Notes:

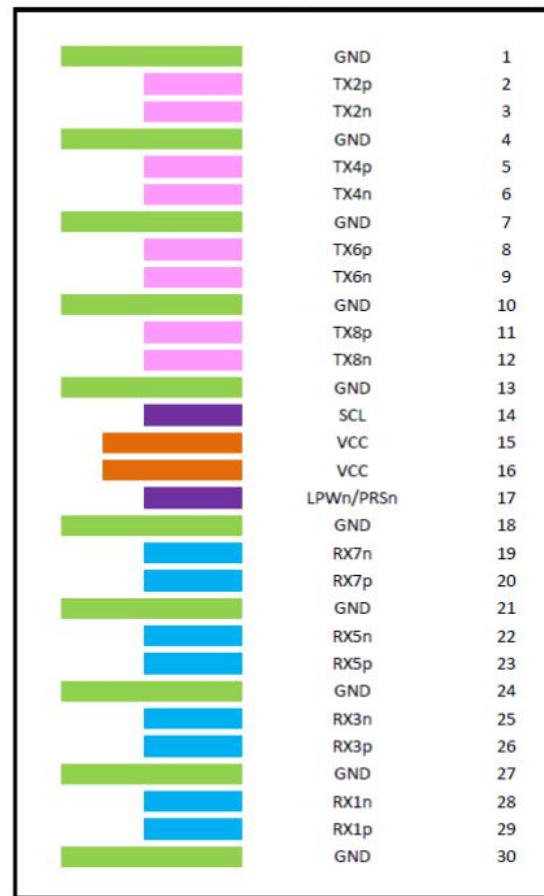
1. Differential input voltage amplitude is measured between TxnP and TxnN
2. Differential output voltage amplitude is measured between RxnP and RxnN.
3. BER=2.4E-4; PRBS31@53.125Gbps. Pre-FEC

Electrical Pad Layout (OSFP end)

Top Side (viewed from top)



Bottom Side (viewed from bottom)



Pin Definition (OSFP end)

Pin	Symbol	Name/Description
1	GND	Ground
2	TX2p	Transmitter Data Non-Inverted
3	TX2n	Transmitter Data Inverted
4	GND	Ground
5	Tx4p	Transmitter Data Non-Inverted
6	TX4n	Transmitter Data Inverted
7	GND	Ground
8	TX6p	Transmitter Data Non-Inverted
9	TX6n	Transmitter Data Inverted
10	GND	Ground
11	TX8p	Transmitter Data Non-Inverted
12	TX8n	Transmitter Data Inverted

13	GND	Ground
14	SCL	2-wire Serial interface clock
15	VCC	+3.3V Power
16	VCC	+3.3V Power
17	LPWn/PRSn	Low-Power Mode / Module Present
18	GND	Ground
19	RX7n	Receiver Data Inverted
20	RX7p	Receiver Data Non-Inverted
21	GND	Ground
22	RX5n	Receiver Data Inverted
23	RX5p	Receiver Data Non-Inverted
24	GND	Ground
25	RX3n	Receiver Data Inverted
26	RX3p	Receiver Data Non-Inverted
27	GND	Ground
28	RX1n	Receiver Data Inverted
29	RX1p	Receiver Data Non-Inverted
30	GND	Ground
31	GND	Ground
32	RX2p	Receiver Data Non-Inverted
33	RX2n	Receiver Data Inverted
34	GND	Ground
35	RX4p	Receiver Data Non-Inverted
36	RX4n	Receiver Data Inverted
37	GND	Ground
38	RX6p	Receiver Data Non-Inverted
39	RX6n	Receiver Data Inverted
40	GND	Ground
41	RX8p	Receiver Data Non-Inverted
42	RX8n	Receiver Data Inverted
43	GND	Ground
44	INT/RSTn	Module Interrupt / Module Reset
45	VCC	+3.3V Power
46	VCC	+3.3V Power
47	SDA	2-wire Serial interface data
48	GND	Ground
49	TX7n	Transmitter Data Inverted
50	TX7p	Transmitter Data Non-Inverted
51	GND	Ground
52	TX5n	Transmitter Data Inverted
53	TX5p	Transmitter Data Non-Inverted

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8 Jinxiu Middle Road, Pingshan, Shenzhen, Guangdong, 518118, P. R. China
+86 755 32983688 | info@china-tscom.com | www.china-tscom.com



54	GND	Ground
55	TX3n	Transmitter Data Inverted
56	TX3p	Transmitter Data Non-Inverted
57	GND	Ground
58	TX1n	Transmitter Data Inverted
59	TX1p	Transmitter Data Non-Inverted
60	GND	Ground

Transceiver Electrical Pad Layout (QSFP56-DD end)

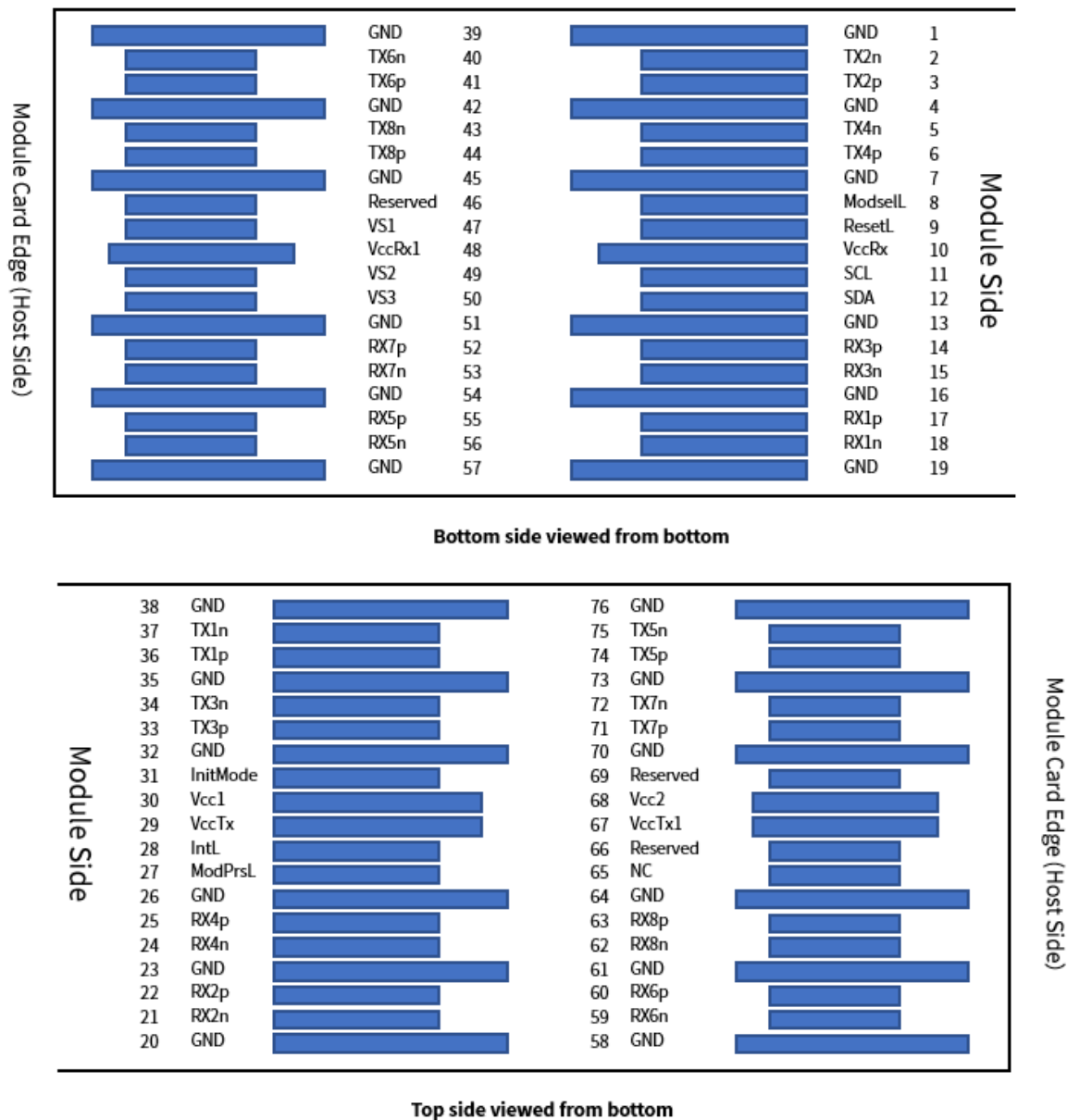


Figure 1 – QSFP-DD Compliant 76-pin connector (per QSFP-DD MSA)

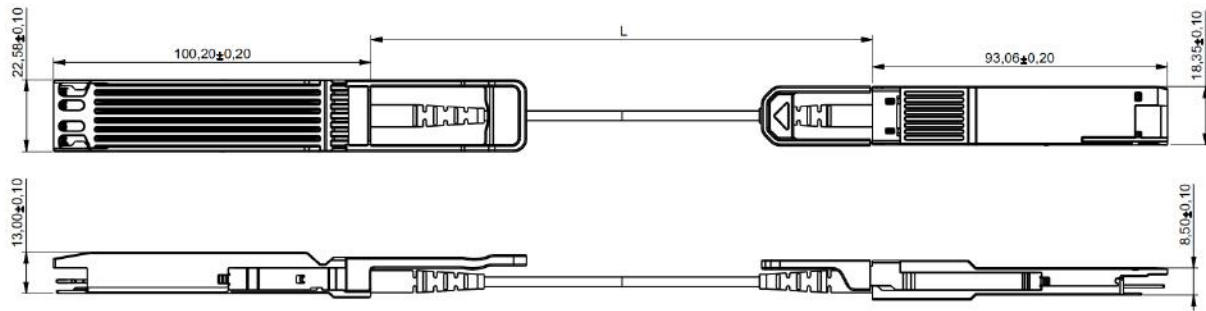
Pin Definition (QSFP56-DD end)

Pin	Symbol	Name/Description
1	GND	Module Ground
2	Tx2-	Transmitter inverted data input
3	Tx2+	Transmitter non-inverted data input
4	GND	Module Ground
5	Tx4-	Transmitter inverted data input
6	Tx4+	Transmitter non-inverted data input
7	GND	Module Ground
8	MODSEIL	Module Select
9	ResetL	Module Reset
10	VCCR _x	+3.3V Receiver Power Supply
11	SCL	2-wire Serial interface clock
12	sda	2-wire Serial interface data
13	GND	Module Ground
14	RX3+	Receiver non-inverted data output
15	RX3-	Receiver inverted data output
16	GND	Module Ground
17	RX1+	Receiver non-inverted data output
18	RX1-	Receiver inverted data output
19	GND	Module Ground
20	GND	Module Ground
21	RX2-	Receiver inverted data output
22	RX2+	Receiver inverted data output
23	GND	Module Ground
24	RX4-	Receiver inverted data output
25	RX4+	Receiver non-inverted data output
26	GND	Module Ground
27	ModPrsL	Module Present, internal pulled down to GND
28	IntL	Interrupt output, should be pulled up on host board
29	VCCT _x	+3.3V Transmitter Power Supply
30	VCC1	+3.3V Power Supply
31	InitMode	Initialization mode; In legacy QSFP applications, the InitMode pad is
32	GND	Module Ground
33	Tx3+	Transmitter non-inverted data input
34	Tx3-	Transmitter inverted data input
35	GND	Module Ground
36	Tx1+	Transmitter non-inverted data input
37	Tx1-	Transmitter inverted data input
38	GND	Module Ground

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39	GND	Module Ground
40	Tx6-	Transmitter inverted data input
41	Tx6+	Transmitter non-inverted data input
42	GND	Module Ground
43	Tx8-	Transmitter inverted data input
44	Tx8+	Transmitter non-inverted data input
45	GND	Module Ground
46	Reserved	For future use
47	VS1	Module Vender Specific 1
48	VCCR _{x1}	+3.3V Power Supply
49	VS2	Module Vender Specific 2
50	VS3	Module Vender Specific 3
51	GND	Module Ground
52	RX7+	Receiver non-inverted data output
53	RX7-	Receiver inverted data output
54	GND	Module Ground
55	RX5+	Receiver non-inverted data output
56	RX5-	Receiver inverted data output
57	GND	Module Ground
58	GND	Module Ground
59	RX6-	Receiver inverted data output
60	RX6+	Receiver inverted data output
61	GND	Module Ground
62	RX8-	Receiver inverted data output
63	RX8+	Receiver non-inverted data output
64	GND	Module Ground
65	NC	NO Connect
66	Reserved	For future use
67	VCCT _{x1}	+3.3V Power Supply
68	VCC2	+3.3V Power Supply
69	Reserved	For future use
70	GND	Module Ground
71	Tx7+	Transmitter non-inverted data input
72	Tx7-	Transmitter inverted data input
73	GND	Module Ground
74	Tx5+	Transmitter non-inverted data input
75	Tx5-	Transmitter inverted data input
76	GND	Module Ground

Mechanical



AOC product bagging circle size:

L	Disc fiber diameter (mm)	number of circle
0.5m	Based on actual circling	1.5
1m		3.5
1.5m	90± Inner diameter, outer diameter ≤110	4.5
2m	100± Inner diameter, outer diameter ≤120	5.5
2.5m	110± Inner diameter, outer diameter ≤130	6.5
3m	100± Inner diameter, outer diameter ≤120	8.5
3m<L≤5m	110± Inner diameter, outer diameter ≤160	Not required
5m<L≤7m	110± Inner diameter, outer diameter ≤170	
7m<L≤30m	110± Inner diameter, outer diameter ≤180	
30m<L≤50m	110± Inner diameter, outer diameter ≤210	
50m<L≤100m	110± Inner diameter, outer diameter ≤250	

Length tolerance table:

L	Tolerance (mm)
$L \leq 1 \text{ M}$	+70/-0
$1 \text{ M} < L < 7 \text{ M}$	+100/-0
$L \geq 7 \text{ M}$	+2% L/-0

Ordering Information

Part Number	Product Description
TSBSD-85N-XXXC	800G OSFP to QSFP-DD AOC 0°C ~ +70°C
XXX :001~030, 1~30 Length in meters on OM3 MMF	

Important Notice

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